



# TTEP 2025

## TEST TECHNOLOGY EDUCATIONAL PROGRAM 2025

### Call for Tutorial Proposals

### (ITC-India, ITC, ATS/ITC-Asia, and LATS)

The Tutorials & Education Group (TEG) of the IEEE Computer Society Test Technology Technical Council (TTTC) organizes in 2025 a comprehensive set of Test Technology Tutorials to be held in conjunction with TTTC-sponsored technical meetings. The objective of this common call is to invite submissions for tutorial proposals to enable the selection of the best-fitted tutorials for each technical meeting as part of the annual Test Technology Educational Program (TTEP).

The tutorials accepted by the Program Committee will be included in the Test Technology Educational Program, intending to serve the test and design professionals offering fundamental education and expert knowledge in state-of-the-art test technology topics.

Participation in TTEP-organized tutorials is *accredited* by the Test Technology Technical Council. Each full-day tutorial corresponds to four TTEP units (half-day tutorial corresponds to two TTEP units). Upon completion of every sixteen units official accreditation in the form of the “IEEE TTTC Test Technology Certificate” is presented to the participants.

The TTEP 2025 tutorials program includes (but is not limited to) the following technical meetings:

- **International Test Conference – India (ITC-India’25)**
- **International Test Conference (ITC’25)**
- **Asian Test Symposium / International Test Conference – Asia (ATS/ITC-Asia’25)**
- **Latin American Test Symposium (LATS’26)**

TTEP accommodates a wide range of technical areas, from mature test topics of high interest to industrial test engineers to emerging test topics with an emphasis on novelty. TTEP is soliciting new and updated tutorial proposals, as well as proposals for Test Clinics, which are particularly geared towards newcomers to the area of testing, such as new test engineers and students pursuing graduate studies in testing, with an objective of offering a broad yet comprehensive review of basic test topics in an accessible way to the lay audience. The topics of interest for the year 2025 TTEP Tutorials include (but are not limited to):

- |                                    |                                |                                      |
|------------------------------------|--------------------------------|--------------------------------------|
| ▪ 3D chiplet testing               | ▪ Failure analysis techniques  | ▪ Reliability and automotive testing |
| ▪ Artificial Intelligence for test | ▪ Functional safety            | ▪ Secure DFT                         |
| ▪ Automatic test equipment         | ▪ High-speed interface testing | ▪ System-level testing               |
| ▪ Board-level testing              | ▪ Memory testing               | ▪ Test economics                     |
| ▪ Built-in self-test (BIST)        | ▪ Mixed-Signal/Analog testing  | ▪ Test resource partitioning         |
| ▪ Data analytics                   | ▪ Nanometer technology testing | ▪ Test related standards             |
| ▪ Defect oriented testing          | ▪ On-line and In-field testing | ▪ Testing machine learning engines   |
| ▪ Design for testability           | ▪ Performance/Delay testing    | ▪ Verification and validation        |
| ▪ Diagnosis and debug              | ▪ Microprocessor testing       | ▪ Wafer testing                      |
| ▪ Embedded core testing            | ▪ Power issues in testing      | ▪ Yield optimization and test        |

**Submissions:** All tutorial proposal submissions to TTEP 2025 are to be made electronically (in PDF format using the TTEP tutorial proposal template provided on both the TTEP main website and the submission website) through the TTEP submissions website:

<http://ttep.ttcc-events.org/ttep/submission.html>

**Deadline for tutorial proposals: May 1st, 2025.**

#### Contact Information:

##### TTEP Chair:

Elena Ioana Vatajelu, TIMA, FR  
E: [ioana.vatajelu@univ-grenoble-alpes.fr](mailto:ioana.vatajelu@univ-grenoble-alpes.fr)

##### TTEP Program Chair:

Riccardo Cantoro, Politecnico di Torino, IT  
E: [riccardo.cantoro@polito.it](mailto:riccardo.cantoro@polito.it)

##### TTTC Liaison:

Yervant Zorian, Synopsys, US  
E: [yervant.zorian@synopsys.com](mailto:yervant.zorian@synopsys.com)

TTEP central web site: <http://ttep.ttcc-events.org/ttep/>

TTEP web site for submissions: <http://ttep.ttcc-events.org/ttep/submission.html>